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U.S. PATENT AND TRADEMARK OFFICE	
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ISSUE CLASSIFICATION

JC 2
JC 852

U.S. UTILITY Patent Application

MAILED	O.I.P.E.	PATENT DATE
AC 003 NS ①	SCANNED	Q.A.

APPLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS	ART UNIT	EXAMINER
09/725048		600	407	3747	Ferko

APPLICANTS:
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ODLAND

TITLE

Process for fabricating integrated circuit devices having a substrate body portion and/or a product disposed thereon.

PTO-2040
12/98

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ISSUING CLASSIFICATION					
ORIGINAL		CROSS REFERENCE(S)			
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)		
INTERNATIONAL CLASSIFICATION					
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TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
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<input checked="" type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent No. <u>pending</u>	(Primary Examiner) _____ (Date) _____			ISSUE FEE	
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	(Legal Instruments Examiner) _____ (Date) _____			Amount Due	Date Paid
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